FIG. 1

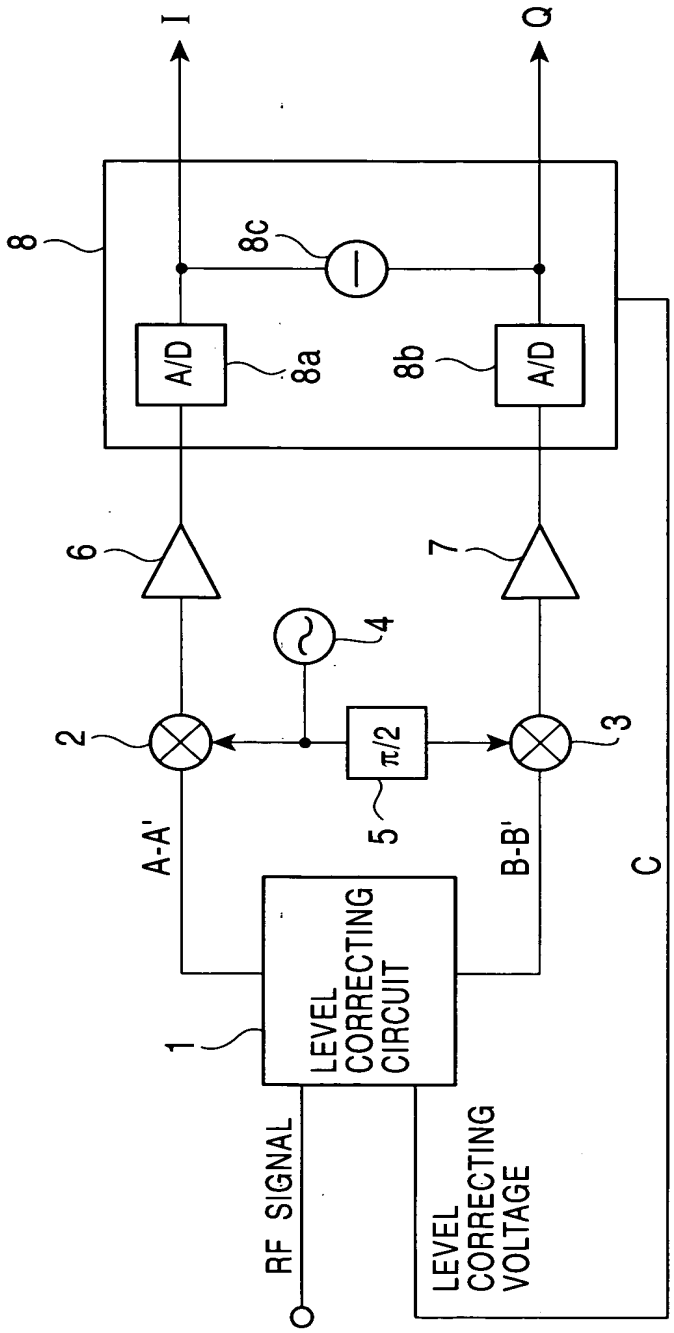
I
Q
8
8c
A/D
8a
8b
A/D
6
7
4
2
3
π/2
5
A-A'
B-B'
C
1
LEVEL CORRECTING CIRCUIT
RF SIGNAL
LEVEL CORRECTING VOLTAGE

FIG. 2

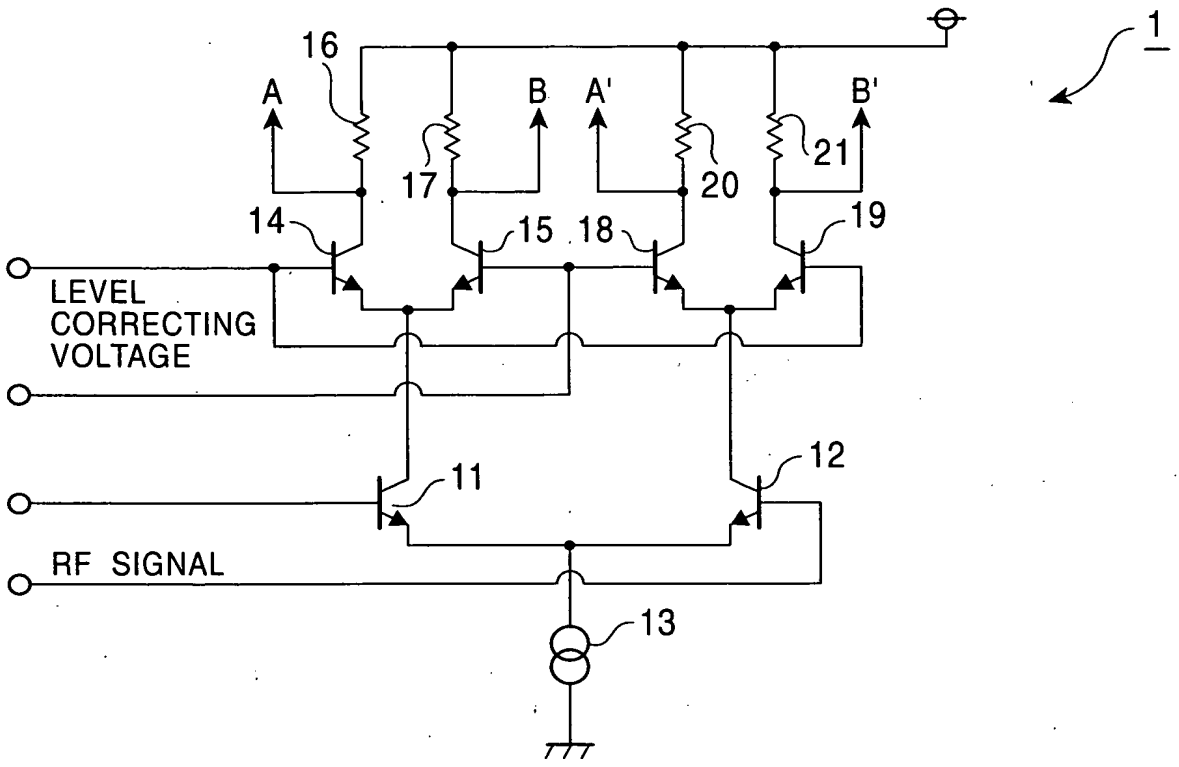
1
16
A
B
A'
B'
17
20
21
14
15
18
19
LEVEL
CORRECTING
VOLTAGE
11
12
RF SIGNAL
13

FIG. 3
PRIOR ART